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IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently amended) A system for displaying information, comprising:

an extended bus bridge, said extended bus bridge including a first portion and a second portion, said first portion comprising a first local bus based on a first protocol bus standard and a first interface to convert a serial signal into said first protocol bus standard, said second portion comprising a second local bus based on said first protocol bus standard and a second interface to convert said first protocol bus standard into said serial signal, said first protocol bus standard defined by a standard of a local internal bus of a computer;

a graphics adaptor coupled to said extended bus bridge at the first portion of said extended bridge;

a central processing unit (CPU) coupled to said second portion of said extended bridge;

a monitor coupled to said graphics adaptor to display the information, such that said graphics adaptor is localized to said monitor and said graphics adaptor and said monitor comprise a display unit; and

a serial link for coupling together said first and second portions of said extended bus bridge, such that at least one of a data bandwidth bottleneck between said graphics adaptor and said monitor and an electromagnetic radiation due to an analog/digital transmission line for display data between said graphics adaptor and said monitor is reduced.

2-5 (Canceled)

6. (Previously presented) The system according to claim 1, wherein said serial link comprises at least one of a cable, a radio frequency (RF) link, and an infrared (IR) link.

7. (Original) The system according to claim 1, wherein said extended bus bridge comprises a peripheral component interconnect (PCI) bus bridge.

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8. (Original) The system according to claim 1, wherein said extended bus bridge comprises an Accelerated Graphics Port (AGP) bus bridge.

9. (Currently amended) A display unit, comprising:

at least a portion of an extended bus bridge, said portion comprising a first local bus based on a protocol bus standard and an interface to convert a serial signal into said protocol bus standard, said protocol bus standard defined by a standard of a local internal bus of a computer, said serial signal received as an input signal into said display unit;

a graphics adaptor coupled to said at least portion of the extended bus bridge; and
a monitor coupled to said graphics adaptor to display the information, such that said graphics adaptor is localized to said monitor and said graphics adaptor and said monitor thereby comprise said display unit, such that at least one of a data bandwidth bottleneck between said graphics adaptor and said monitor and an electromagnetic radiation due to an analog/digital transmission line for display data between said graphics adaptor and said monitor is reduced.

10. (Original) The display unit according to claim 9, wherein said at least a portion of said extended bus bridge comprises one side of said extended bus bridge embedded in said adaptor.

11. (Original) The display unit according to claim 9, wherein said extended bus bridge includes first and second portions, a first portion being coupled to said graphics adaptor.

12. (Original) The display unit according to claim 9, wherein said extended bus bridge comprises a peripheral component interconnect (PCI) bus bridge.

13. (Original) The system according to claim 9, wherein said extended bus bridge comprises an Accelerated Graphics Port (AGP) bus bridge.

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14. (Currently amended) A method of decreasing a bottleneck in a communications bus, comprising:

coupling a graphics adaptor, a central precessing unit (CPU) and a display monitor over said communications bus;

providing an extended bus bridge between said graphics adaptor and said central processing unit (CPU) to comprise said communication bus; and

localizing said graphics adaptor to said display monitor ~~and~~ such that said graphics adaptor and said monitor comprise a display unit,

wherein said extended bus bridge includes a first portion and a second portion, said first portion comprising a first local bus based on a first ~~protocol~~ bus standard and a first interface to convert a serial signal into said first ~~protocol~~ bus standard, said second portion comprising a second local bus based on said first ~~protocol~~ bus standard and a second interface to convert said first ~~protocol~~ bus standard into said serial signal, said first ~~protocol~~ bus standard defined by a standard of a local internal bus of a computer,

wherein the first portion is coupled to said graphics adaptor and the second portion is coupled to said CPU, and

wherein the first portion and the second portion are coupled to each other via a serial link,

such that at least one of a data bandwidth bottleneck between said graphics adaptor and said monitor and an electromagnetic radiation due to an analog/digital transmission line for display data between said graphics adaptor and said monitor is reduced.

15-18. (Canceled)

19. (Previously presented) The method according to claim 14, wherein said link comprises at least one of a cable, a radio frequency (RF) link, and an infrared (IR) link.

20. (Original) The method according to claim 14, wherein said extended bus bridge comprises a peripheral component interconnect (PCI) bus bridge.

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21. (Original) The method according to claim 14, wherein said extended bus bridge comprises an Accelerated Graphics Port (AGP) bus bridge.
22. (Previously Presented) The system according to claim 1, wherein said display unit including said first portion of said extended bus bridge is housed in a first enclosure,
wherein said CPU coupled to said second portion of said extended bus bridge is housed in a second enclosure, and
wherein said first and second enclosures are physically separated and connected by a set of wires.
23. (Previously Presented) The method according to claim 14, wherein said display unit including said first portion of said extended bus bridge is housed in a first enclosure,
wherein said CPU coupled to said second portion of said extended bus bridge is housed in a second enclosure, and
wherein said first and second enclosures are physically separated and connected by a set of wires.
24. (Previously Presented) The system according to claim 1, wherein subsequent to a refresh operation said serial link is for carrying only information that changes.
25. (Previously Presented) The system according to claim 14, wherein subsequent to a refresh operation said serial link is for carrying only information that changes.
26. (Previously presented) The system according to claim 1, wherein the localized coupling between said graphics adapter and said monitor comprises a parallel interconnection, thereby permitting a faster interconnection.